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PATENT

Practitioner's Docket No. IDT-1548



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chuen-Der Lien

Serial No.: 09/185,057

Group No.: unknown

Filed: 11/02/1998

Examiner: unknown

For: "Low-Power Content Addressable Memory Cell"

Assistant Commissioner for Patents
Washington, D.C. 20231-0001

Date: November 16, 1998

REQUEST FOR CORRECTED FILING DATE

Applicant requests correction of the filing date stamped on the return postcard in the above application *in order to avoid the generating of an erroneous Filing Receipt*:

1. Attached is a copy of the return receipt postcard stamped by the PTO in the above application for which issuance of a corrected filing date is respectfully requested.
2. There is an error with respect to the following, which is incorrectly entered.

Error in

1. Filing Date

Correct data

1. 11/02/1998

3. A copy of the Express Mail Label EL188845055US clearly showing that the U.S. Post Office received the above application on November 2, 1998 is also enclosed.
4. The correction is not due to any error by applicant and no fee is due.

Respectfully submitted,

E. Eric Hoffmann,
Attorney for Applicant
Reg. No. 38,186

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Assistant Commissioner for Patents
BOX PATENT APPLICATION
Washington, D.C. 20231-0001

1st Inventor: Chuen-Der Lien
Assignee: Integrated Device Technology, Inc.
Title: "Low-Power Content Addressable Memory Cell"

Dkt. No: IDT-1548

ENCLOSED:

1 page - Utility Patent Application Transmittal (PTO/SB/05)
1 page - Fee Transmittal in duplicate (PTO/SB/17)
Pages of Specification and Drawings: APP/37; DRWGS/14 (informal)
2 pages - original, executed Combined Declaration And Power Of Attorney
1 page - Assignment Recordation Cover Sheet (Form PTO 1595)
4 pages - original, executed Assignment Of Invention
1 page - Information Disclosure Statement By Applicant (PTO/SB/08A)
1 copy of each cited reference (8 total)
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